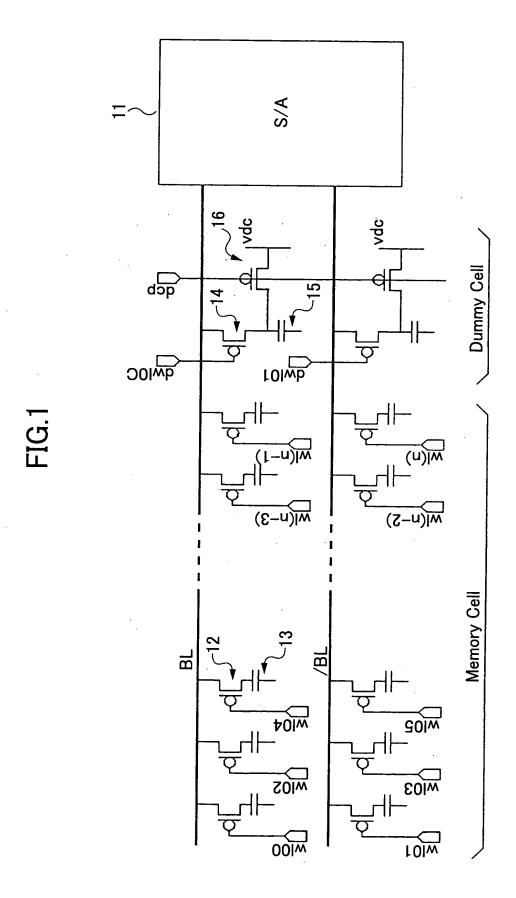
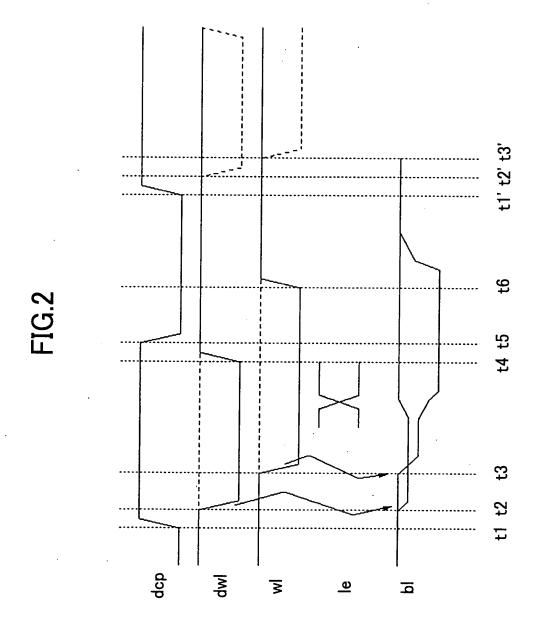
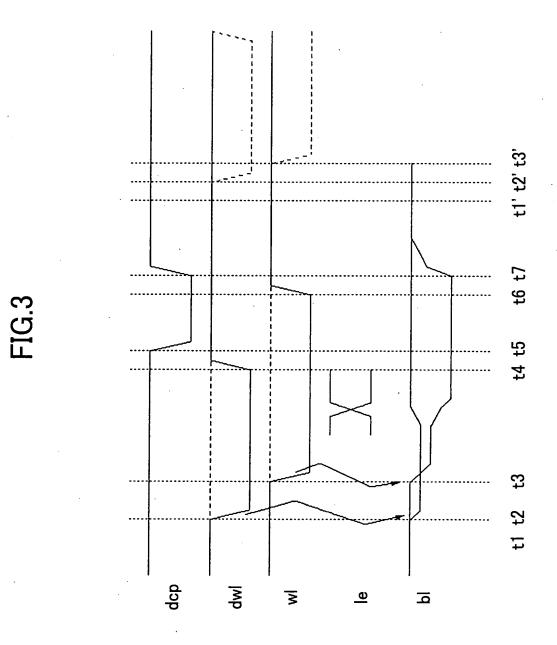
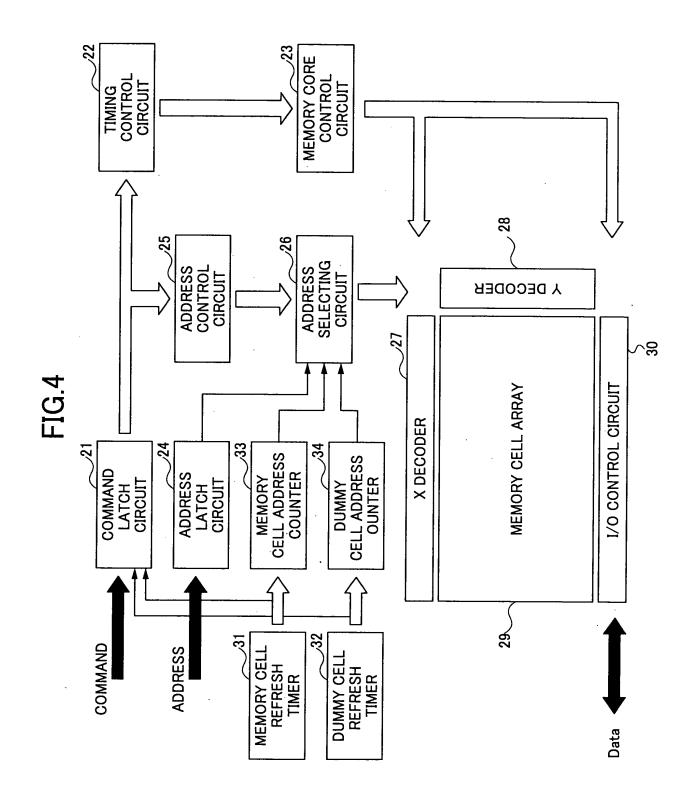
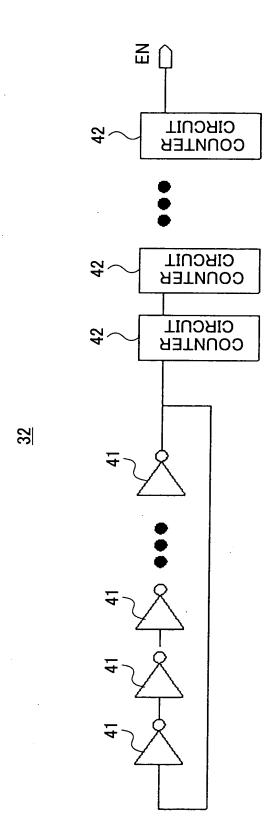
METHOD

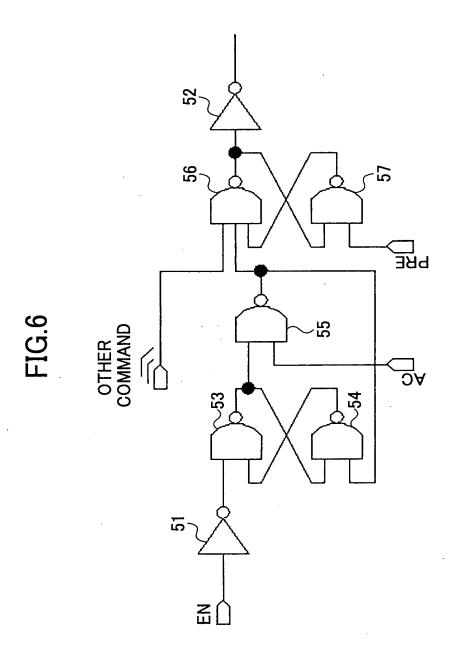


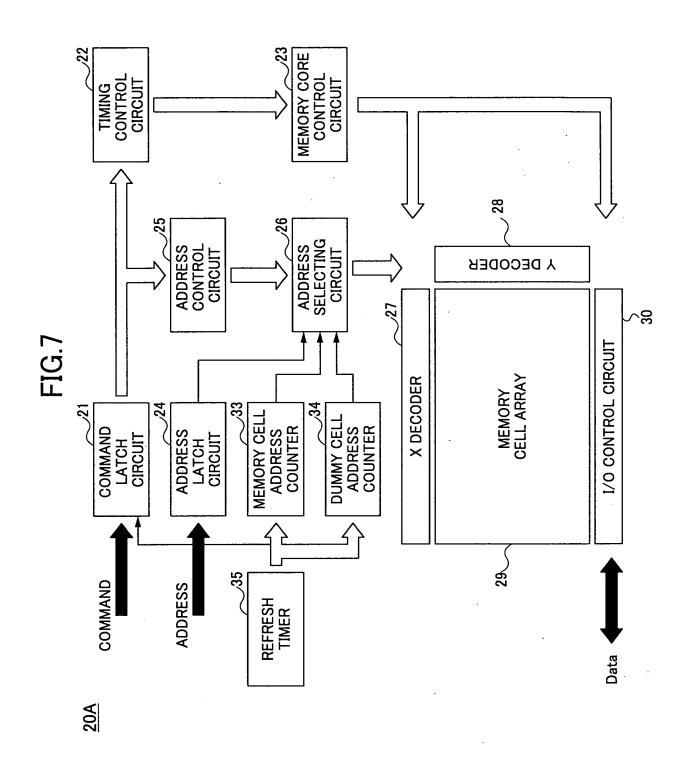




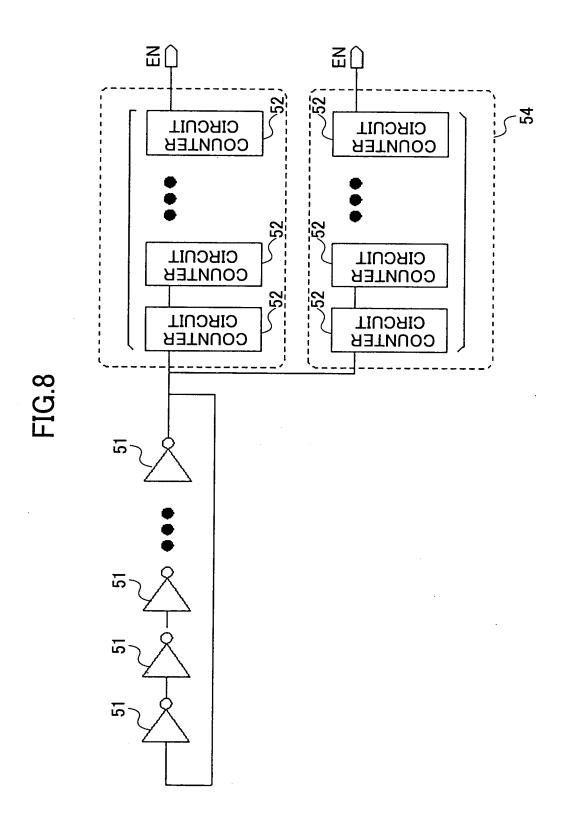




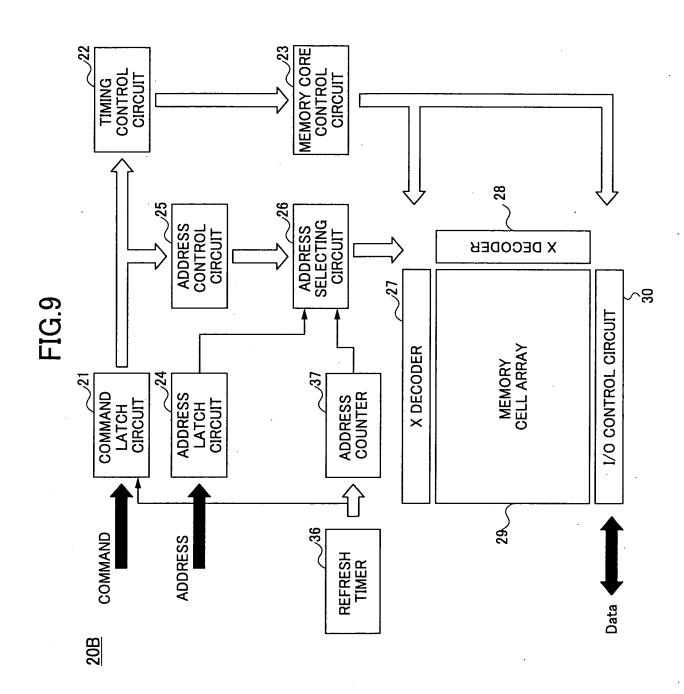




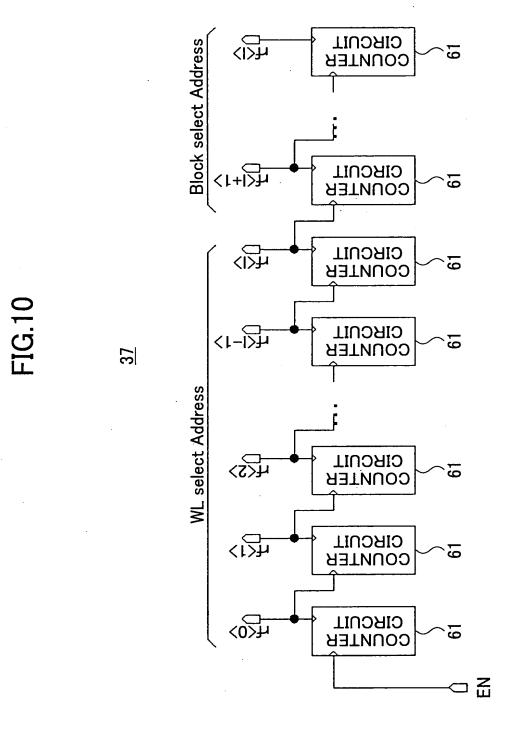
METHOD

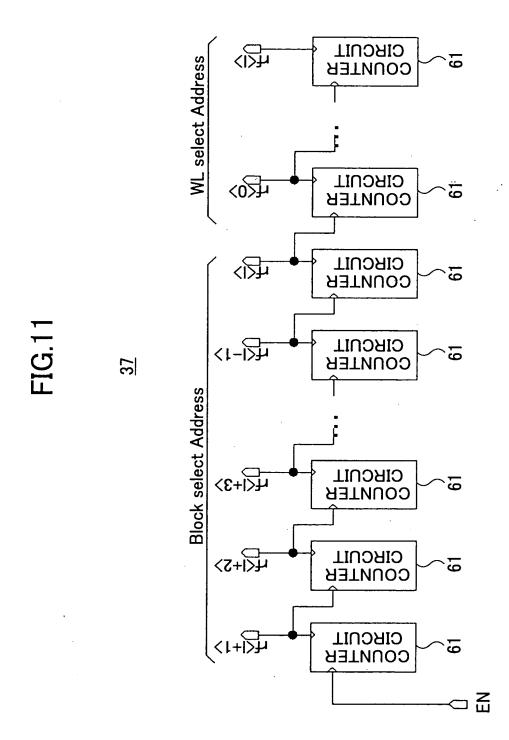


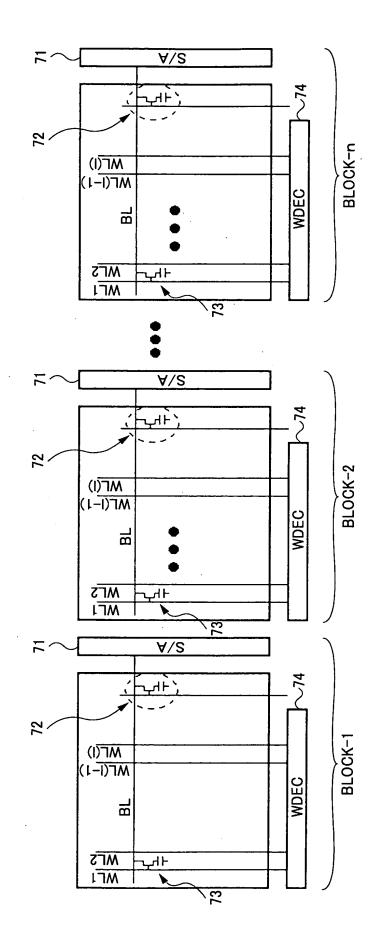
Title: SEMICONDUCTOR MEMORY DEVICE BASED ON DUMMY-CELL METHOD Inventor: Masato TAKITA et al. Docket Number: 100353-00175

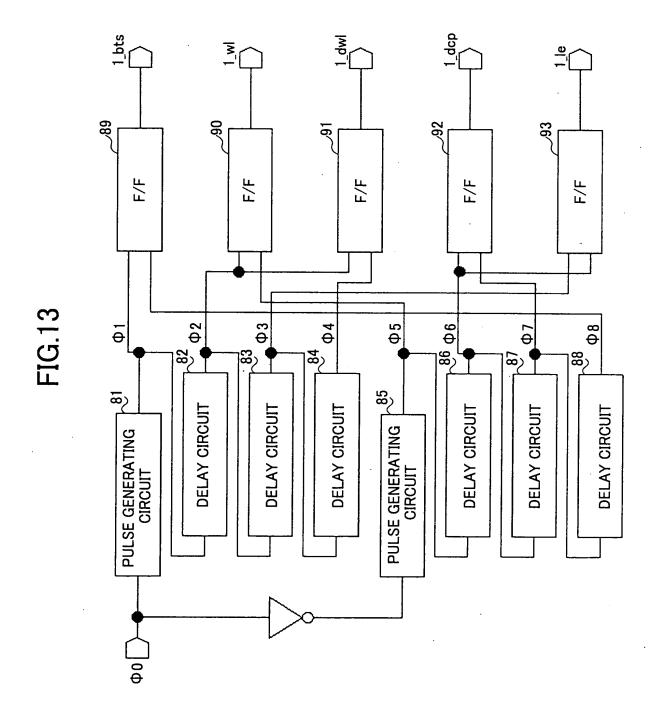


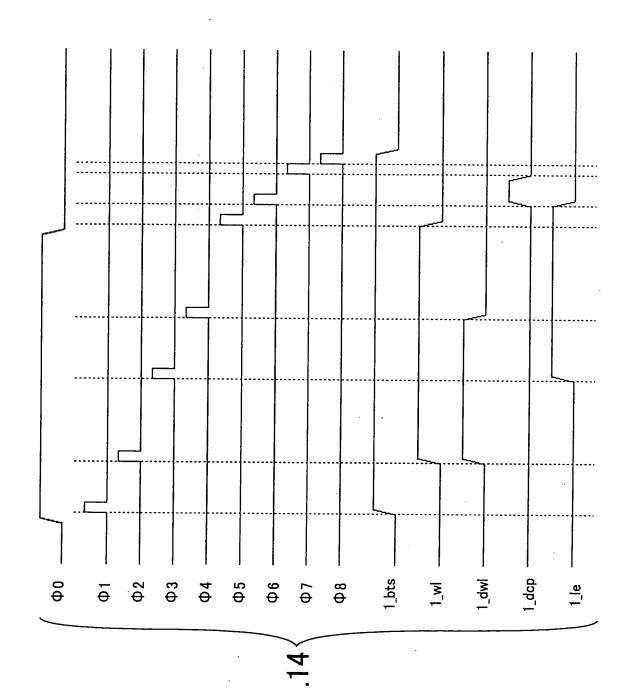
METHOD











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Title: SEMICONDUCTOR MEMORY DEVICE BASED ON DUMMY-CELL

METHOD